FIG. 1

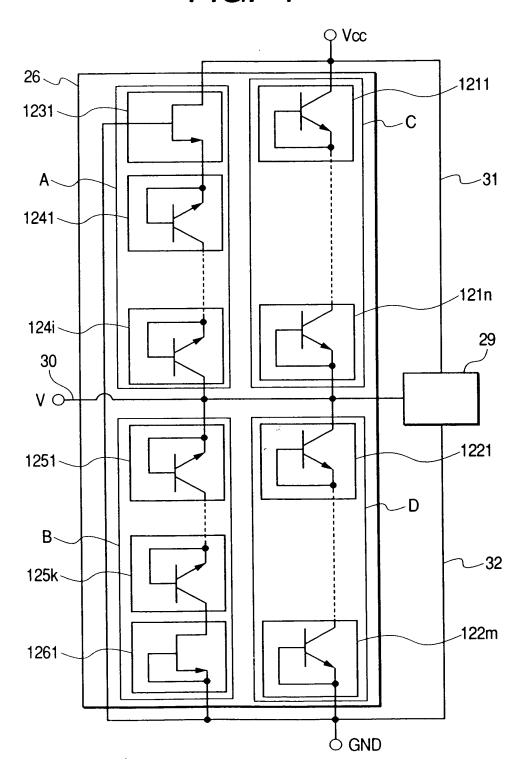


FIG. 2(A)

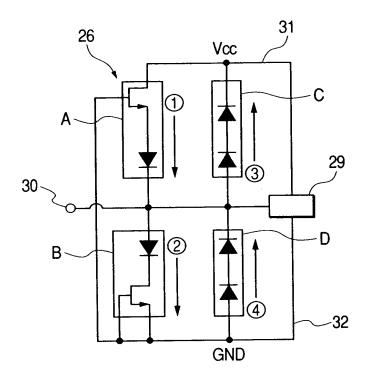


FIG. 2(B)

No.	NOTED PIN & POTENTIAL DIFFERENCE	CURRENT PATH
1	Vcc(+), GND(−)	1-2
2	V∞(+), S(−)	①
3	S(+), GND(-)	2
4	V∞(−), S(+)	3
5	V∞(−), GND(+)	4-3
6	S(-), GND(+)	4

FIG. 3

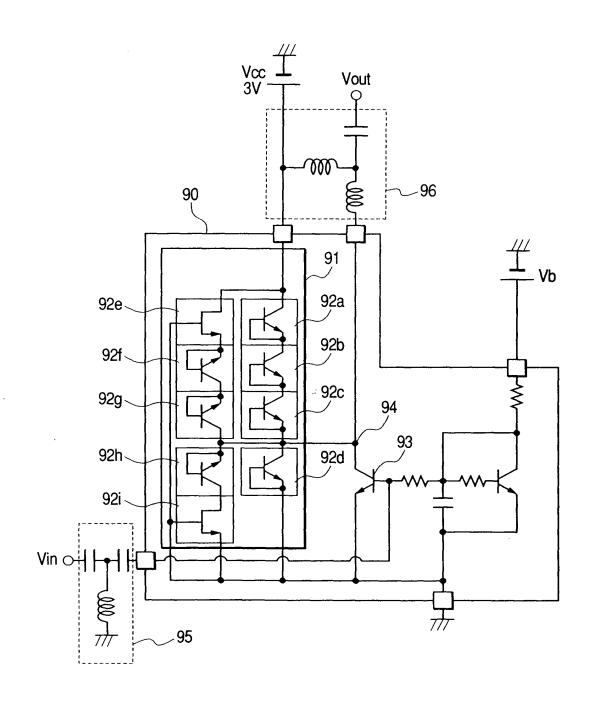


FIG. 4

O : ONE-STAGE STACK OF TRANSISTORS CONNECTED WITH POWER SUPPLY IN PROTECTIVE CIRCUIT

♥ : THREE-STAGE STACK OF TRANSISTORS CONNECTED WITH POWER SUPPLY IN PROTECTIVE CIRCUIT 15 f=945MHz 10 OUTPUT POWER (dBm) 5 0 **COMPRESSION** POINT OF 1 dB -5 -10 -15-8 -15 -30 -35 -5 -25 -20 -15 -10 0 INPUT POWER (dBm)

FIG. 5

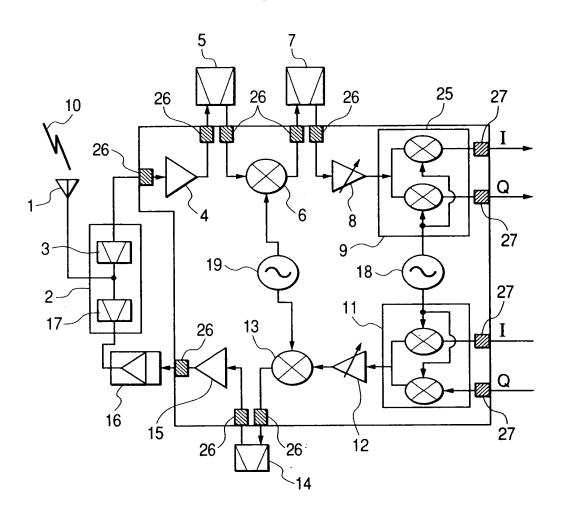


FIG. 6

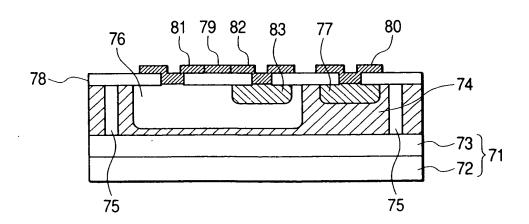


FIG. 7

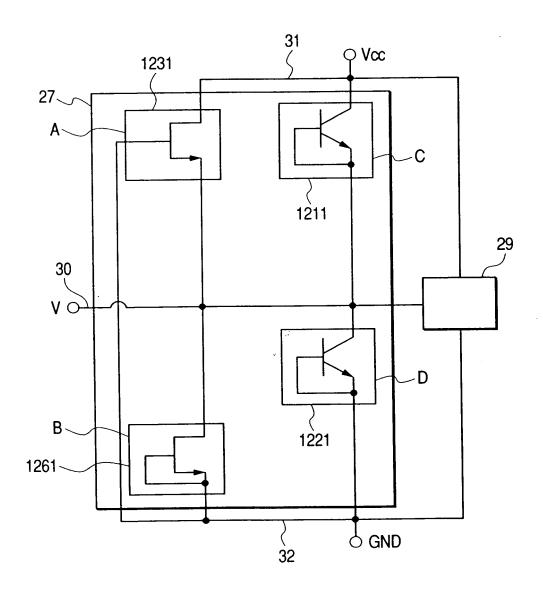


FIG. 8

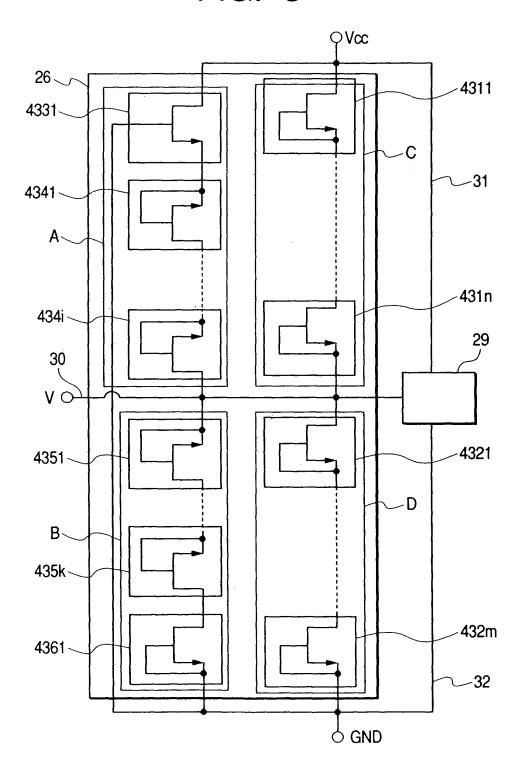


FIG. 9

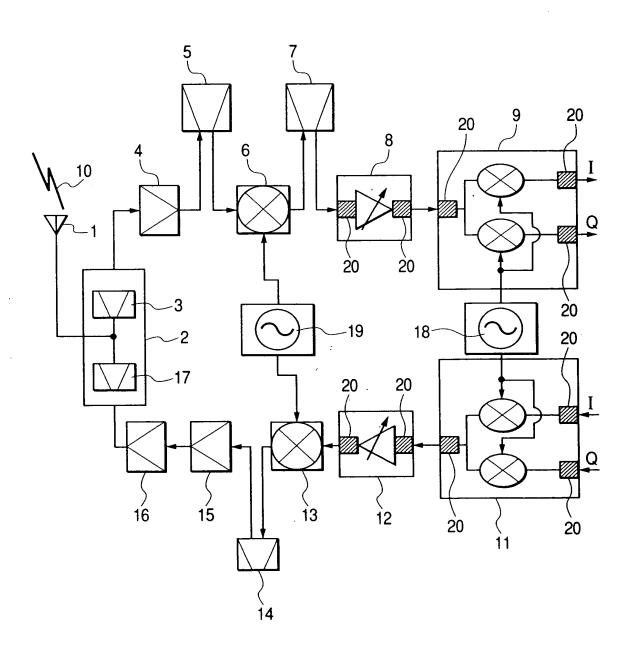


FIG. 10

41

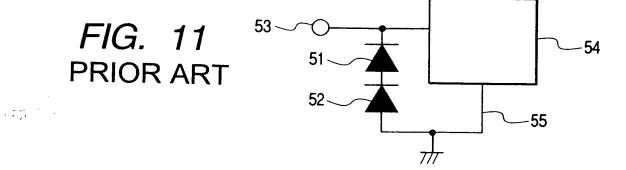
42

45

43

41

45



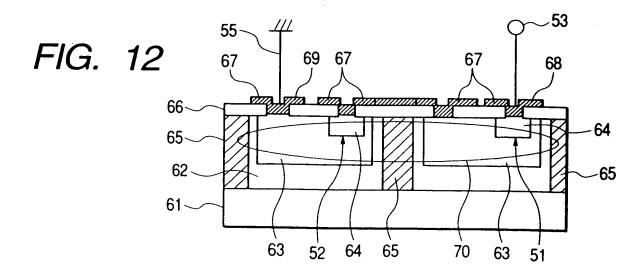


FIG. 13(A)

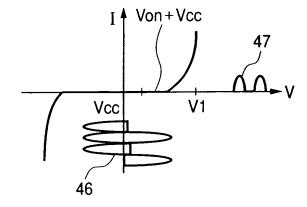


FIG. 13(B)

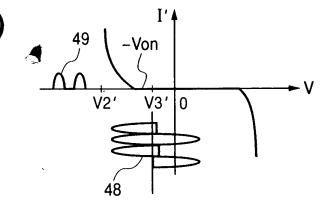


FIG. 14

